

FIG. 1

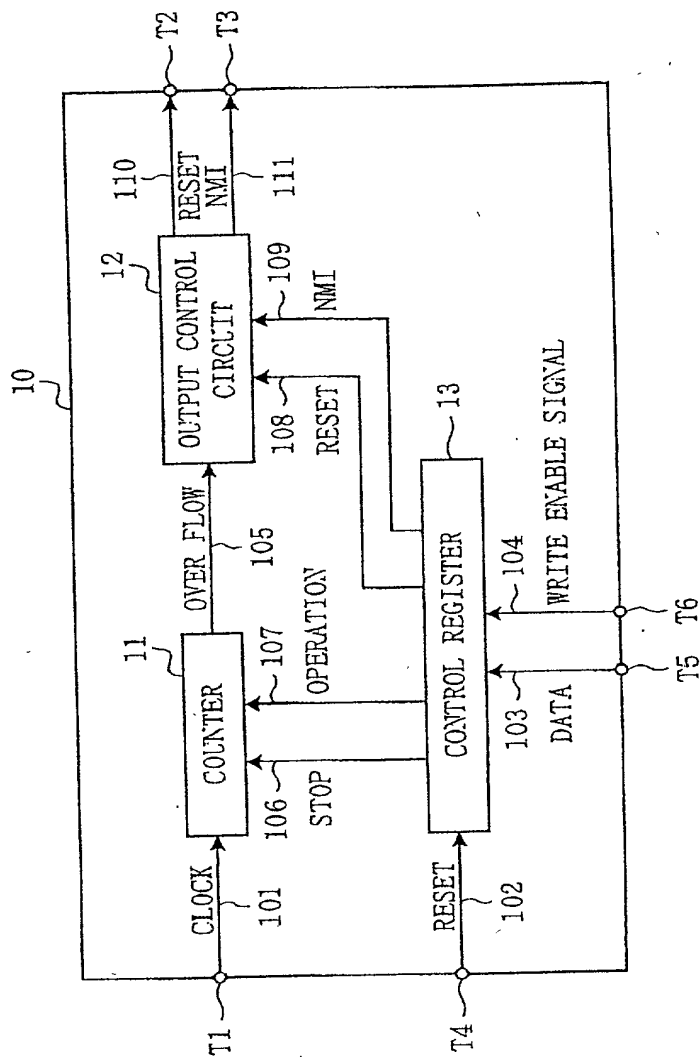


FIG. 2

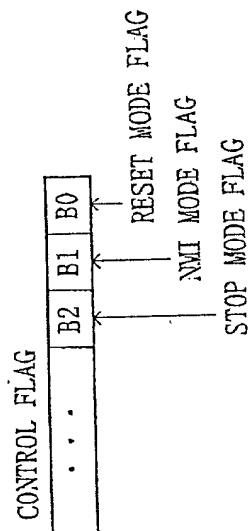


FIG. 3A

OPERATIONAL MODE	B2	B1	B0
RESET MODE	X	X	0
NMI MODE	X	0	1
STOP MODE	0	1	1

X: DON'T CARE

FIG. 3B

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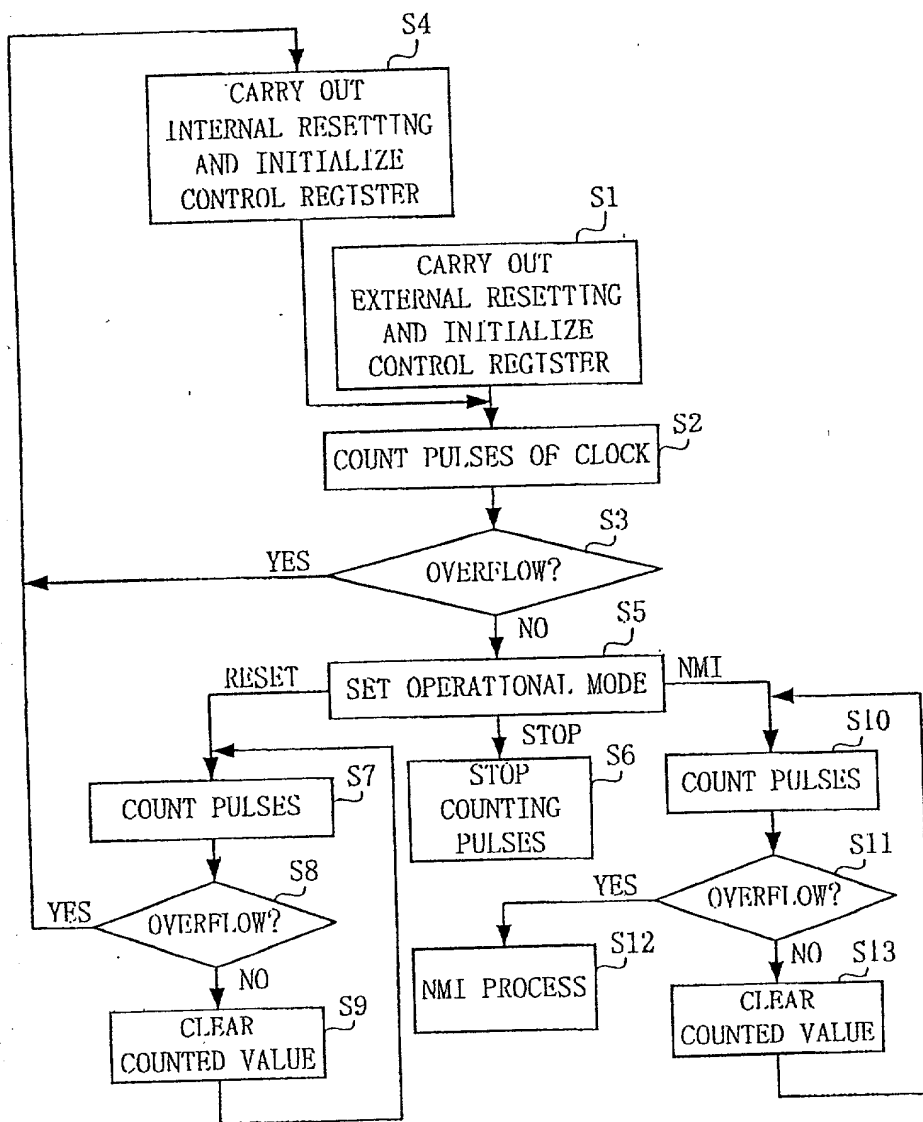


FIG. 4

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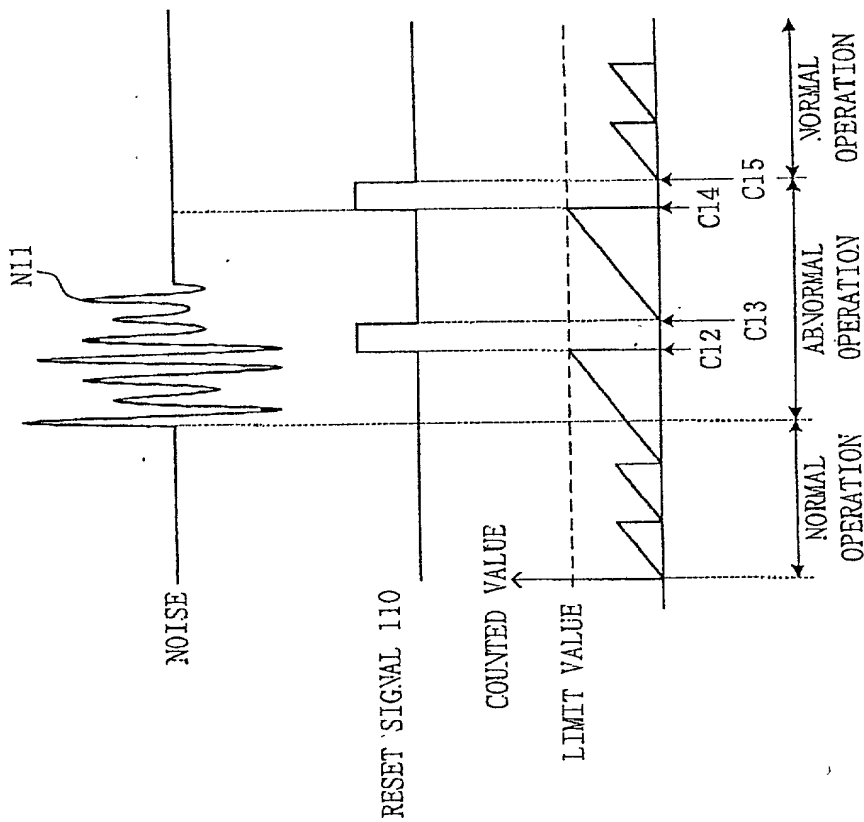


FIG. 5

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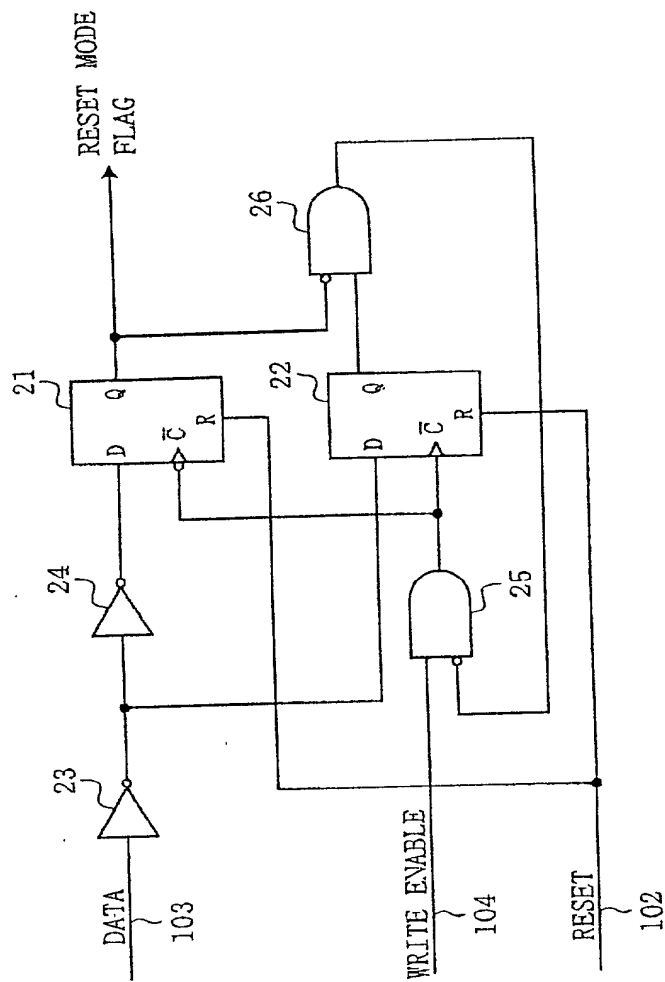


FIG. 6

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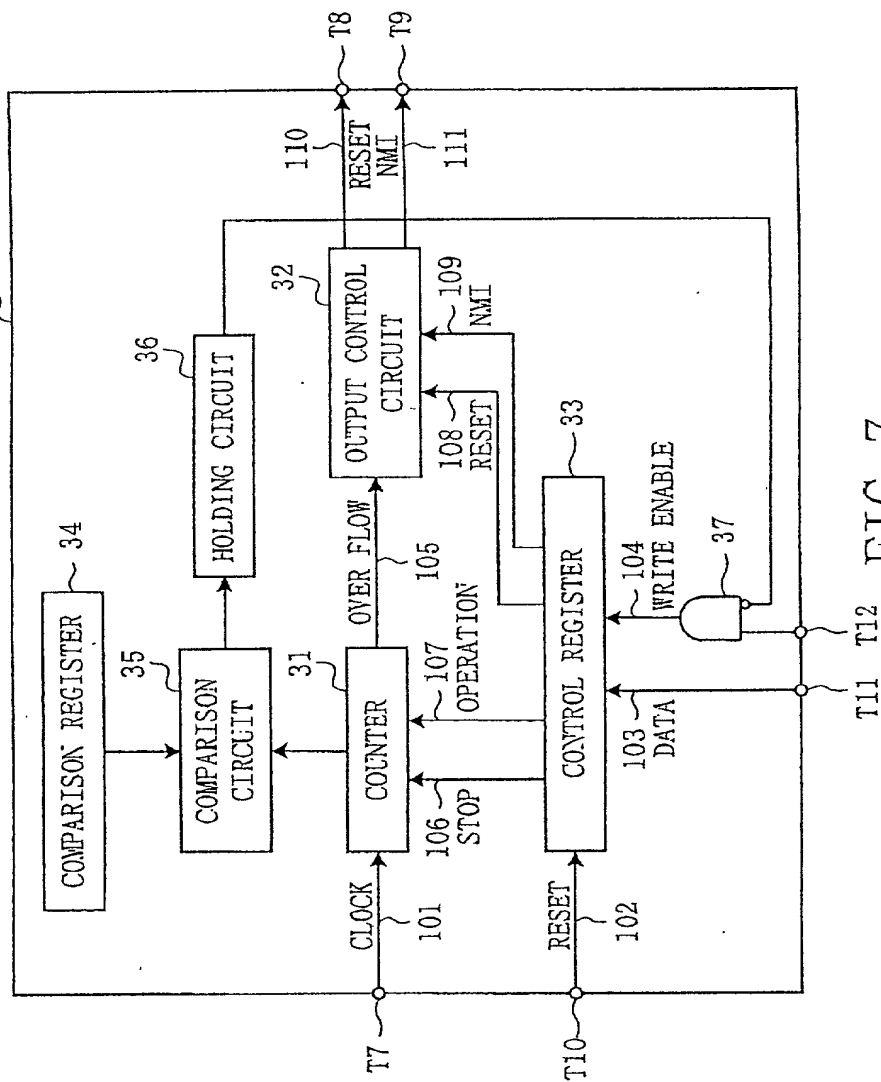


FIG. 7

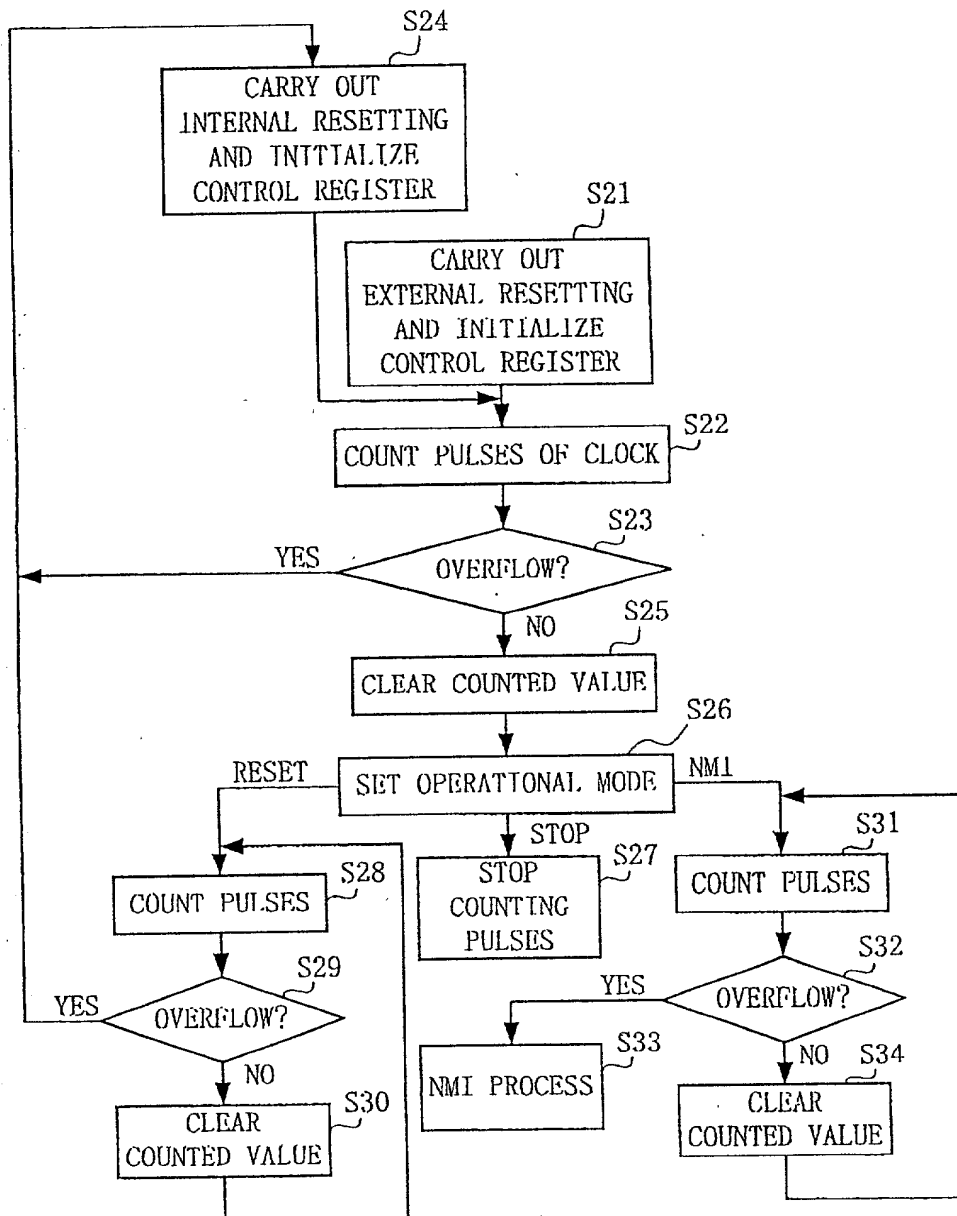


FIG. 8

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	ABNORMAL OPERATION	FACTOR
A	<ul style="list-style-type: none"> <li>• TRANSISTOR (Tr) DOES NOT OPERATE</li> <li>• CLOCK FOR WDT IS NOT PROVIDED</li> </ul>	<ul style="list-style-type: none"> <li>• BREAKAGE OF POWER SOURCE LINE</li> <li>• BREAKAGE OF OSCILLATOR</li> </ul>
B	<ul style="list-style-type: none"> <li>• Tr OPERATES</li> <li>• CLOCK FOR WDT IS PROVIDED</li> <li>• SOFTWARE DOES NOT RUN</li> </ul>	<ul style="list-style-type: none"> <li>• ABNORMAL OPERATION IN CPU, ROM, RAM, ETC. (SUCH AS VOLTAGE DROP IN CPU)</li> </ul>
C	<ul style="list-style-type: none"> <li>• Tr OPERATES</li> <li>• CLOCK FOR WDT IS PROVIDED</li> <li>• SOFTWARE RUNS</li> <li>• TEMPORARY ABNORMALITY</li> </ul>	<ul style="list-style-type: none"> <li>• COMMON NOISE (E. G. IN POWER SOURCE)</li> <li>• ERROR IN SOFTWARE DESIGN (E. G. UNDEFINED CONDITION)</li> </ul>

FIG. 9

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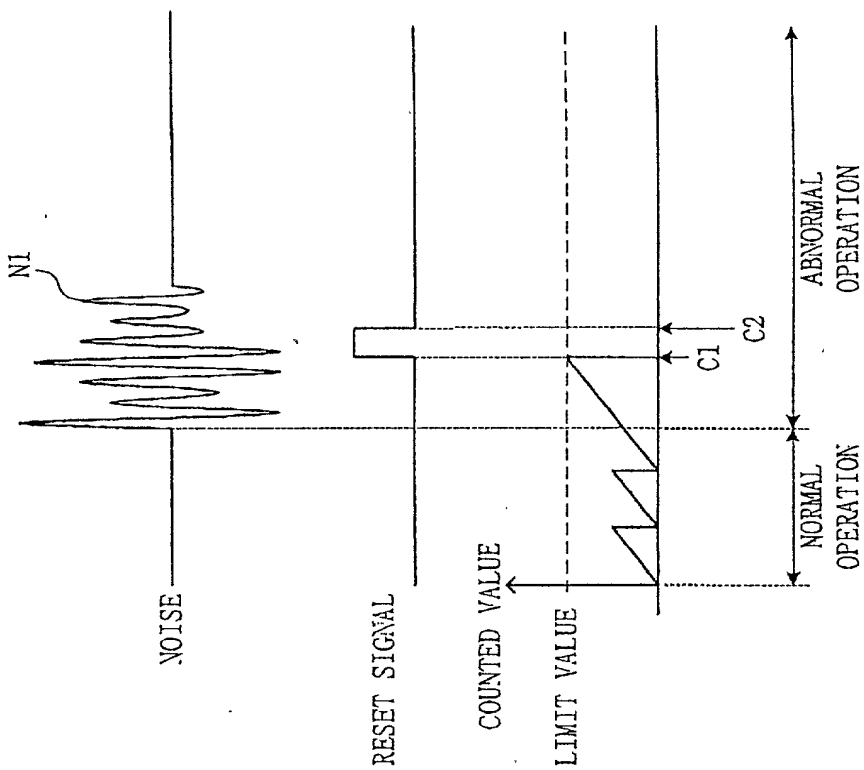


FIG. 10